



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Fastow et al.

Serial No.: 10/738,322

Filed: December 16, 2003

For: FLASH MEMORY WITH
BURIED BIT LINES

Examiner: WOJCIECHOWICZ, E.

Art Unit: 2815

Confirmation No.: 7930

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO AN OFFICE ACTION

Dear Sir:

In response to the Office Action mailed August 24, 2005, please consider the following remarks and enter the following amendments. Amendments to the claims are presented beginning on page 2 of this response. Remarks begin on page 5.

AMD-H0563/JPH/WAZ
Examiner: WOJCIECHOWICZ, E.

1

Serial No.: 10/738,322
Group Art Unit: 2815